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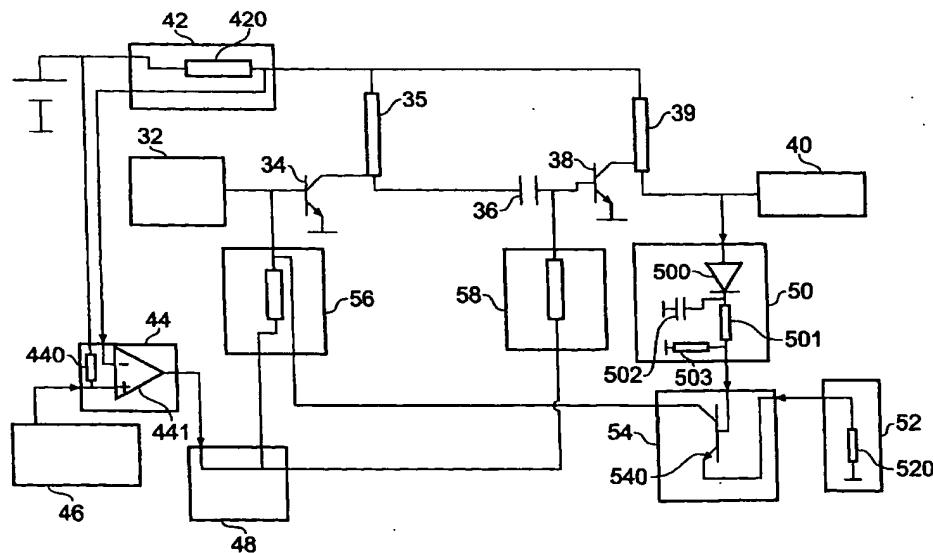
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(54) Title: CIRCUIT FOR POWER AMPLIFICATION



(57) Abstract: A radio frequency (RF) power amplifier, including an external control loop and a protection circuit, which functions to maintain a constant output power during variations in impedance on the RF load. In the external control loop a collector current from an output transistor is detected and then regulated with respect to a reference current. The regulated signal is utilised to generate a bias control signal which is input to the base electrode of both a driver transistor and the output transistor. The protection circuit detects a voltage envelope at the collector electrode of the output transistor and utilises this signal to form a bias reduction signal which is input to the base electrode of the driver transistor.

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INTERNATIONAL SEARCH REPORT

International Application No

PCT/EP 03/07948

A. CLASSIFICATION OF SUBJECT MATTER
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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H03F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	US 4 924 191 A (ERB LEE A ET AL) 8 May 1990 (1990-05-08) column 5, line 50 - column 9, line 2; figure 1	4-6,8 1-3,7
X A	US 6 043 707 A (BUDNIK BRIAN JOSEPH) 28 March 2000 (2000-03-28) column 7, line 39 - column 8, line 9; figure 9	4-6,8 1-3,7
X A	US 5 426 641 A (AFRASHTEH ALIREZA ET AL) 20 June 1995 (1995-06-20) column 15, line 16 - column 16, line 28	4,8 1-3,5-7
A	US 5 673 001 A (KIM JIN DONG ET AL) 30 September 1997 (1997-09-30) column 4, line 50 - column 5, line 44; figure 5	1-8



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Information on patent family members

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